

AMENDMENT TRANSMITTAL LETTER (Large Entity)

Applicant(s): Kenji MOTOMOCHI

Docket No.

L8462.03117

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/713,213	November 17, 2003	A. Williams	52989	2826	2453

Invention: SEMICONDUCTOR DEVICE



COMMISSIONER FOR PATENTS:

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	30 -	30 =	0	x \$50.00	\$0.00
INDEP. CLAIMS	3 -	3 =	0	x \$200.00	\$0.00
Multiple Dependent Claims (check if applicable)	<input type="checkbox"/>				\$0.00
				TOTAL ADDITIONAL FEE FOR THIS AMENDMENT	\$0.00

No additional fee is required for amendment.

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The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account 19-4375

Any additional filing fees required under 37 C.F.R. 1.16.

Any patent application processing fees under 37 CFR 1.17.

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Signature

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Dated: September 18, 2006

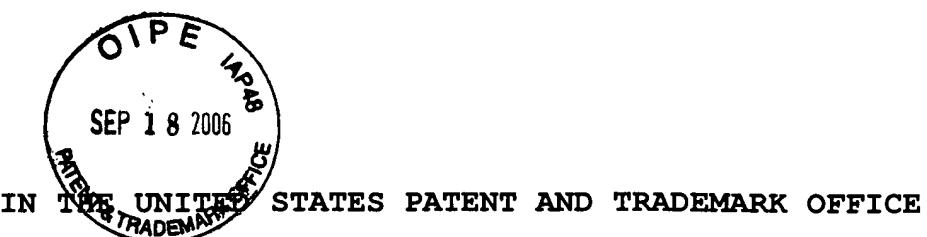
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on _____

(Date)

Signature of Person Mailing Correspondence

Typed or Printed Name of Person Mailing Correspondence

CC:



In re the Application of

Inventor: Kenji MOTOMOCHI Group Art Unit: 2826
Appln. No.: 10/713,213 Examiner: A. Williams
Filed: November 17, 2005
For: SEMICONDUCTOR DEVICE HAVING A CHIP-CHIP STRUCTURE
(as amended)

AMENDMENT UNDER 37 CFR § 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated June 16, 2006, please
amend the above-captioned application as follows:



IN THE TITLE

Please amend the title to read as follows:

SEMICONDUCTOR DEVICE HAVING A CHIP-CHIP STRUCTURE